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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/809,507	03/26/2004	Ryogo Yanagisawa	2004-0472A	8594
513 7590 02/15/2008 WENDEROTH, LIND & PONACK, L.L.P. 2033 K STREET N. W. SUITE 800 WASHINGTON, DC 20006-1021			EXAMINER	
			WANG, HARRIS C	
			ART UNIT	PAPER NUMBER
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*	•		02/15/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)				
• *** **	10/809,507	YANAGISAWA, RYOGO				
Office Action Summary	Examiner	Art Unit				
•	Harris C. Wang	2139				
The MAILING DATE of this communication ap						
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMN 136(a). In no event, however, will apply and will expire SIX (e, cause the application to bec	IUNICATION. may a reply be timely filed i) MONTHS from the mailing date of this communication. me ABANDONED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 21 N	lovember 2007.					
	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under	Ex parte Quayle, 193	5 C.D. 11, 453 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-19 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) Claim(s) is/are allowed. 6) Claim(s) 1-19 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/o	awn from consideratio					
Application Papers						
9) The specification is objected to by the Examin 10) The drawing(s) filed on 26 March 2004 is/are. Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Examin 10.	a) accepted or b) e drawing(s) be held in a ction is required if the d	beyance. See 37 CFR 1.85(a). awing(s) is objected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of: 1. Certified copies of the priority documer 2. Certified copies of the priority documer 3. Copies of the certified copies of the priority documer application from the International Burea * See the attached detailed Office action for a list 	nts have been receivents have been receive ority documents have au (PCT Rule 17.2(a)	d. d in Application No been received in this National Stage				
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	7 — Pa 5)	erview Summary (PTO-413) ner No(s)/Mail Date ice of Informal Patent Application er:				

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DETAILED ACTION

Claims 1-19 are pending

Response to Arguments

Applicant's arguments filed 11/21/2007 have been fully considered but they are not persuasive.

The Applicant argues that the Examiner's position that "it would have been obvious to form the random number generator, the secret key holding unit, the public key generator, and the shared key generator of Goss on one semiconductor unit because the method of Goss has been known in the art for almost two decades, and one of ordinary skill in the art would be able to implement this method on one semiconductor chip" is a conclusory statement of obviousness without providing any explanation as to why one of ordinary skill in the art would have made such a modification. (pg. 11-12 of Remarks)

The Examiner was merely trying to establish that all of the elements of the claimed invention were taught by Goss (random number generator, secret key holding unit, public key generator, shared key generator, and controller) and one of ordinary skill would be able to combine the different units on one semiconductor chip with predictable results.

The Applicant then argues that Goss does not disclose the ability to prevent the eavesdropping of certain values, algorithms and variables. Without addressing whether

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Goss does disclose the ability to prevent eavesdropping of certain values, the Applicant has not claimed "the ability to prevent eavesdropping of certain values," therefore the Examiner considers this argument spurious.

The Examiner believes the above arguments are unpersuasive and repeats his previous rejection.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Goss (4956863).

Regarding Claims 1-2, 5-6, 9-10, 13-14, 18-19

Goss teaches a key exchange apparatus including:

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a random number generator for generating a random number ka that holds a relationship 0 < ka < q, where an element in a finite group F for which multiplication is defined is g and an order that is a prime number of the element g is q; ("The first step in establishing the session key is that each user generates a secret number in a random number generator 14, 16. The numbers are designated Xa, Xb respectively, and are selected from a set of positive integers up to p-1" Column 6, lines 23-27)

a secret key holding unit for temporarily holding the random number ka; ("Storage area 40 contains a preselected number Xa, stored at the time of manufacture of the A device" Column 7, line 52)

a public key generator for calculating a public key ya in the finite group F from the random number ka, the element g, and the prime number q;

 $(Ya = a^{Xa} \mod(p), Yb = a^{Xb} \mod(p) \pmod{p}$ (Column 6, lines 30-35)

and a shared key generator for calculating a shared key Ka in the finite group F using a public key yb generated from a random number kb which holds a relationship 0<kb<q and is generated by a second user as a destination distribution of the shared key, and the random number ka that is held by the secret key holding unit, ("Each user also has a session key generator 18, 20... After the exchange of values Ya, Yb, each user computers a session key K in its session key generator 18, 20 by raising the other user's Y value to the power represented by the user's own X value, all modulo p" Column 6, lines 27-28, 55-60)

a controller of a first user as a distribution source of the shared key controlling the random number generator and the public key generator for obtaining the public key ya, and transmitting the obtained public key ya to a second user as a distribution

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destination of the shared key, and said controller obtaining the public key yb from the second user as the shared key distribution destination, and controlling the shared key generator for deriving the shared key Ka. ("The key management steps previously described proceed automatically under the control of the cryptographic processor 60, and when a session key has been derived, this is automatically applied in a conventional cryptographic process" Column 12, lines 25-30)

Goss does not explicitly teach where at least said random number generator, said secret key holding unit, said public key generator, and the shared key generator being formed on one semiconductor integrated circuit.

It would have been obvious to one of ordinary skill in the art at the time of the invention to implement the method of Goss on one semiconductor integrated circuit.

The motivation is that the method of Goss has been known in the art for almost two decades, and one of ordinary skill in the art would be able to implement this method on one semiconductor integrated circuit.

The methods associated with the apparatus are taught in the cited sections.

Regarding Claim 3, 7, 11,15,

Goss teaches the key exchange apparatus of claim 13.

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Goss does not explicitly teach wherein when the finite group F is an elliptic curve E(F) in a finite field, and an element on the elliptic curve E(F) is G, the public key generator calculates the public key ya on the elliptic curve E(F) using the random number ka, the element G, and the prime number q by a formula: ya=kaG mod q, and the shared key generator calculates the shared key Ka on the elliptic curve E(F) by a formula: Ka=Kayb mod q, using the public key yb=kbG mod q that is generated from the random number kb on the elliptic curve E(F) by the second user as the shared key distribution destination, and the random number ka that is held in the secret key holding unit.

It would have been obvious to one of ordinary skill in the art at the time of the invention to have the finite group F as an elliptic curve E(f) in a finite field, and an element on the elliptic curve E(f) is G.

The motivation is that the Applicant in the background of the invention admits that "An elliptic curve crypto system is widely known as a cryptosystem based on the difficulty in solving the discrete logarithm problem in the finite group F. More specifically, when assuming an elliptic curve in the finite group as E(F) a point on the elliptic curve E(F) which is previously shared by the user 1 and the user 2 as G, and an arithmetic xG using a point x on the epplictic curve E(f) is defined." Therefore one of ordinary skill in the art would be able to use the apparatus of Goss to implement a system using an elliptic curve in a finite field

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Goss teaches the key exchange apparatus of claim 13 wherein the random number generator generates a new random number ka after each new exchange of message traffic. ("Ideally, a new session key should be established for each exchange of message traffic. An additional unsecured exchange is needed to accomplish this. The number generator in the A device generates a random number...and the number generator in the B device generates a random number...these are supplied to the session key generators 18, 20 repectively" column 8, lines 46-54)

However Goss does not explicitly teach generating said random number after the calculation of the shared key Ka or the calculation of the public key Ya, nor does Goss explicitly teach holding the new random number ka in the secret key holding unit.

It would have been obvious to one of ordinary skill in the art at the time of the invention to modify the system of Goss to either regenerate a new random number after the calculation of the shared key Ka or the calculation of the public key Ya, and subsequently store the random number in the storage unit.

The motivation is that it is clear that for each session there requires a new session key which requires a new random number to be generated. Whether the new random number is generated after the shared key Ka or the public key Ya is an obvious modification, to the cited art in Goss which discloses that the new random number must be generated before the start of the next session.

Conclusion

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THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Harris C. Wang whose telephone number is 5712701462. The examiner can normally be reached on M-F 9-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, KRISTINE KINCAID can be reached on (571) 272-4063. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

HCW

Bustine Kincaid Kristine Kincaid Supervisory Patent Examiner

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